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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known					
Application Number	09/650,850				
Filing Date	August 29, 2000				
First Named Inventor	James D. Barnette				
Art Unit	2634				
Examiner Name	Kevin Kim				
Attorney Docket Number	BARNETTE 1				

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Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ^{2 (F known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		^{US-} 6,573,940 B1	06/03/2003	Feng Yang	
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First Named Inventor	James D. Barnette				
Art Unit	2634				
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			U. S. PATEN	DOCUMENTS	
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K		⁰⁵⁻ 5,970,137	10/19/1999	LeDamany, et al.	
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0		^{US-} 6,597,787	07/22/2003	Lindgren, et al.	
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	09/650,850		Ì
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				First Named Inventor	James D. Barnette	1111 0 0 2004	
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				Examiner Name	Kevin Kim	Technology Center	260
Sheet	3	of	3	Attorney Docket Number	J BARNETTE 1		,

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.), title of ne-issue T ²	
K		Floyd M. Gardner "Interpolation in Digital Modems - Part I: Fundamentals" IEEE Transactoins on Communications, Vol. 41, No. 3, March 1993; pp. 501-507		
(C		Lars Erup, et al. "Interpolation in Digital Modems - Part II: Implementation and Performance" IEEE Transactions on Communications, Vol. 41, No. 6, June 1993; pp. 998-1008		
٤		James. D. Barnette, et al.; U.S. Patent Application Serial No. 09/652,116, filed 08/29/2000 entitled "Resampler for a bit Pump and Method of Resampling a Signal Associated Therewith"; pp. 1-59; 6 drawing sheets		
K		Mandeep Singh Chadha, et al.; U.S. Patent Application Serial No. 09/650,854, filed 08/29/2000 entitled "Separation Circuit for an Echo Canceling System and Method of Operating the Same"; pp. 1-67; 6 drawing sheets		
K		Mandeep Singh Chadha, et al.; U.S. Patent Application Serial No. 09/650,853, filed 08/29/2000 entitled "Echo Canceling System for a Bit Pump and Method of Opearting the Same"; pp. 1-60; 5 drawing sheets	*-	
6		Mandeep Singh Chadha, et al.; U.S. Patent Application Serial No. 09/650,851, filed 08/29/2000 entitled "Filter Circuit for a Bit Pump and Method of Configuring the Same" pp. 1-57; 7 drawing sheets		
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